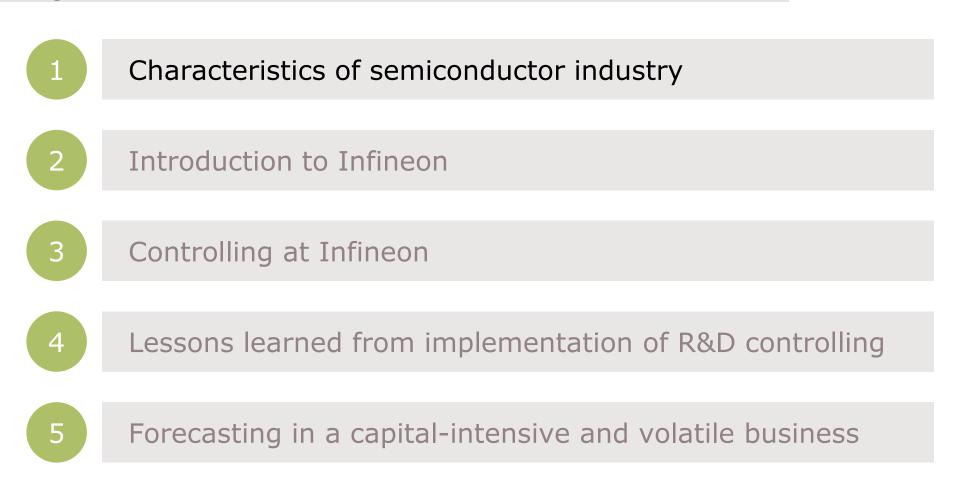
Controlling in highly competitive high-tech industries

Dominik Asam, CFO 16 May 2017







Agenda

More with less: semiconductors are vital building blocks in various crucial areas of life





#### More **energy** with less **resources**



#### More **performance** with less **energy**



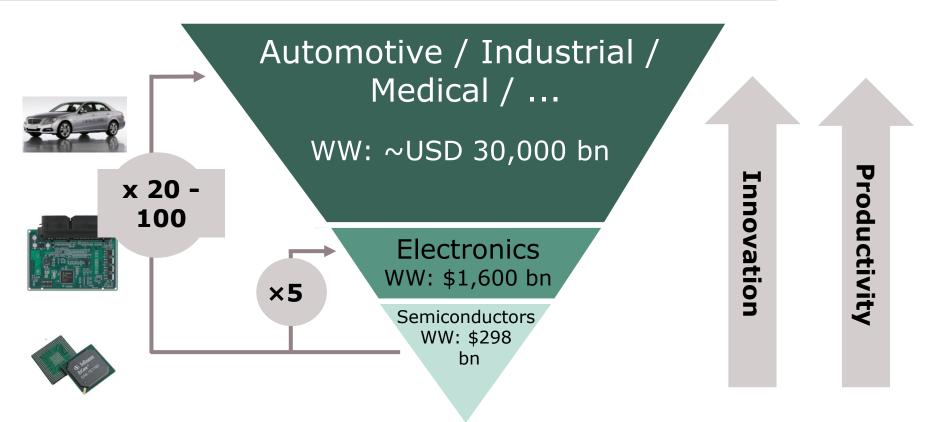
#### More **mobility** – safe and with less **CO**<sub>2</sub>



#### More **security** at lower cost

The semiconductor industry has significant leverage on downstream innovation





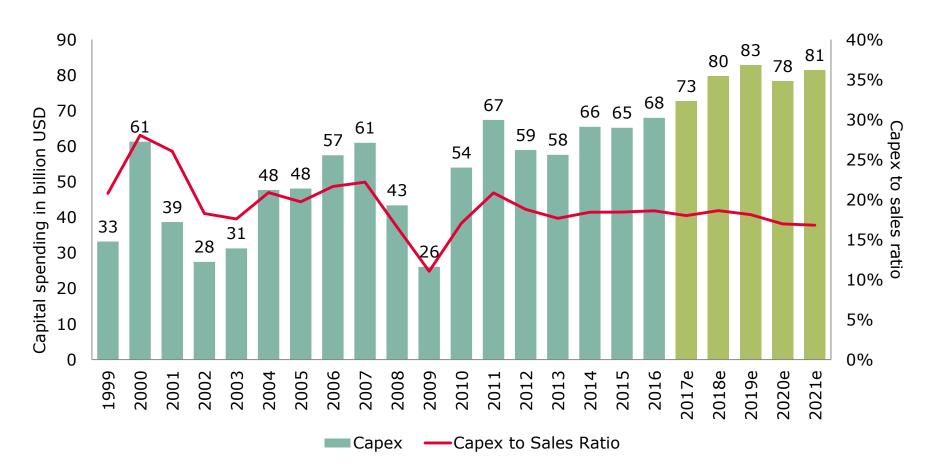
- About 45% of the OECD Economic growth since 1985 comes from increased productivity; electronics is a key driver for this growth
- > Up to 80% of innovation in automotive is enabled by semiconductors, even more when it comes to Hybrid and EV

Source: DECISION, ESIA, Future Horizons, IMF, WSTS 2010, AUDI, OECD Factbook 2013, Infineon

## Capital intensity is slightly declining but very high compared to other industries





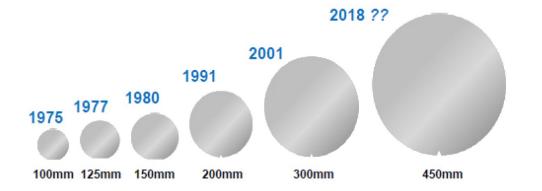


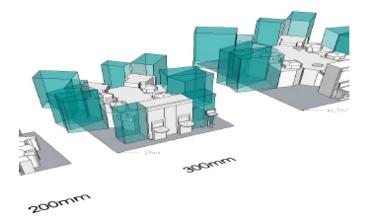
Calendar years. Source: IC Insights, The McClean Report 2016, March 2016.

## Wafer diameter is a key lever for cost reduction but also drives scale of factories



#### Larger wafer diameters





#### Source: Intel, SEMI ISS 2013

💋 semı"

Wafer	Area	
Transition	Increase	
100mm → 125mm	56%	
100mm → 150mm	125%	
125mm → 150mm	44%	
150mm → 200mm	78%	
200mm → 300mm	125%	
300mm → 450mm	125%	

#### SEMI Industry Strategy Symposium (ISS) – January 15, 2013: Intel Corporation executives make first public presentation of 450mm silicon wafer patterned with 26nm features using nano imprint lithography.

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### Shrink of feature size as another key lever for cost reduction hits economical limits



Moore's Law - scaling for more than 20 generations: often assumed to be at the end but survived - now stalling!?

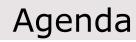
Scaling of Gate Length Cost per Transistor Scaling ransistor  $10^{2}$ 1.0 2.3 billion The end or next transistors 109 MOSFET gate length (µm) 0.8 brakethrough 101 ransistors 108 to come? Cost/ 0.6 10<sup>0</sup> 107 **ITRS** targets processor chip 106 0.4 Vormalized 10-1 0.2 10-2 104 7.4 nm 0.0 10-3 45 40 32 28 20 20 1970 1980 1990 2000 2010 2020 nm FinFET nm nm nm nm Year

No cost/transistor crossover for first time at 28  $\rightarrow$  20nm transition expected. But system integration is still a driver.

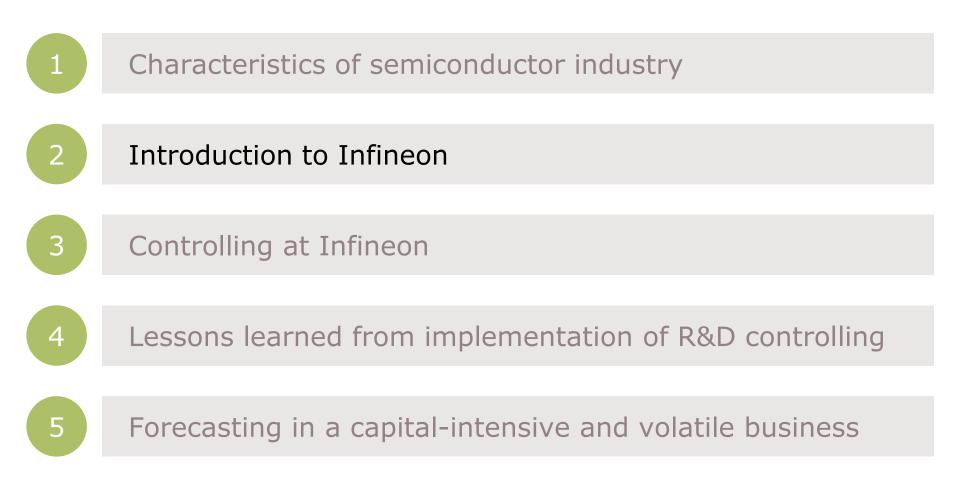
Source: Lisa Su, AMD, ISSCC '13 Keynote

2017-05-16

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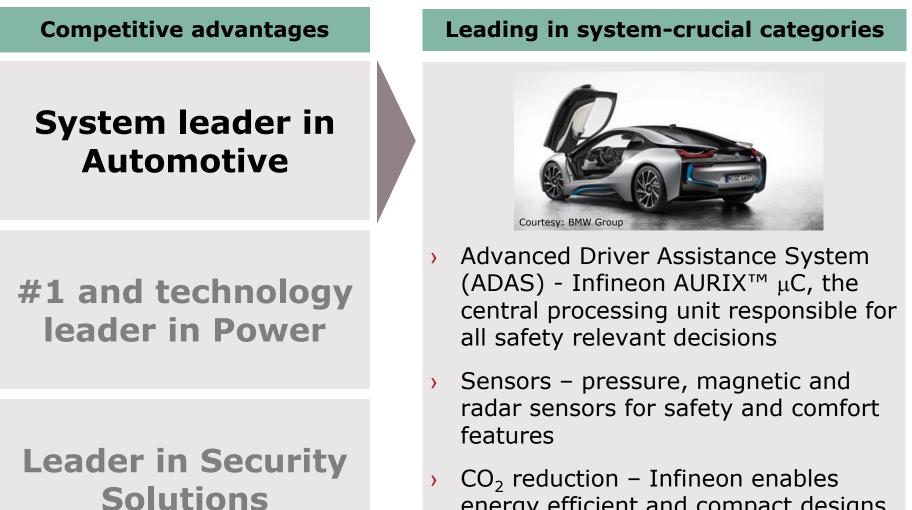






## Leadership in system understanding will foster future growth

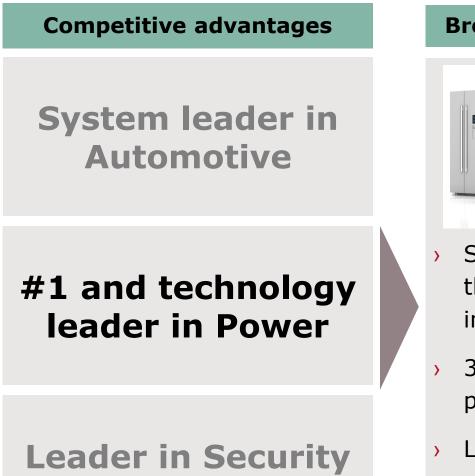




 CO<sub>2</sub> reduction – Infineon enables energy efficient and compact designs in all areas requiring electronics

## Leadership in system understanding will foster future growth





Solutions

#### **Broad product & technology portfolio**



- System leader with digitalization of the control loop and functional integration
- 300mm thin-wafer manufacturing for power semiconductors
- Leader in next-generation power semiconductor materials GaN and SiC

## Leadership in system understanding will foster future growth



#### **Competitive advantages**

### System leader in Automotive

### #1 and technology leader in Power

### Leader in Security Solutions

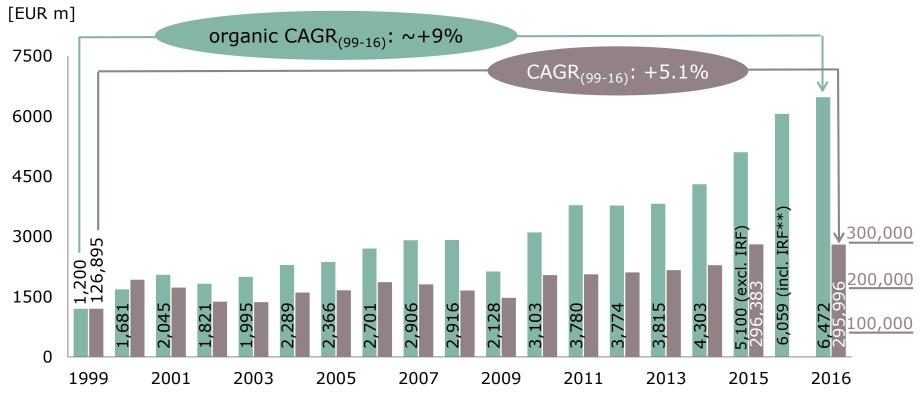
#### **Mobile Communication & IoT Security**



- > Hardware based security for
  - Compliance with security mechanisms determined on application level
  - Secure & trusted environment for data storage and code execution
  - Protection against manipulation, access and theft of secrets
- Machine-to-Machine communication as backbone for reliable operation

## Infineon's revenue development (excl. IRF) outperformed total semi market





Revenue Infineon\* [lhs]

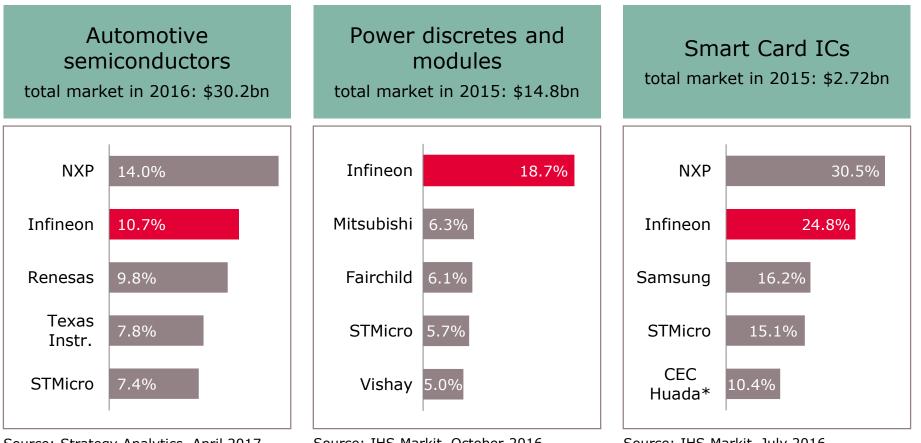
Semiconductor World Market (adjusted for the Infineon fiscal year ending Sep 30) [rhs]

\* Based on Infineon's portfolio (excl. Other Operating Segments and Corporate & Eliminations) per end of FY16.

\*\* If International Rectifier had been consolidated since 1 Oct 2014, Infineon would have recorded revenues of €6,059m in FY15. Source: Infineon; WSTS (World Semiconductor Trade Statistics), November 2016

Infineon increased relative market share in power and outperformed chip card market



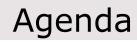


Source: Strategy Analytics, April 2017

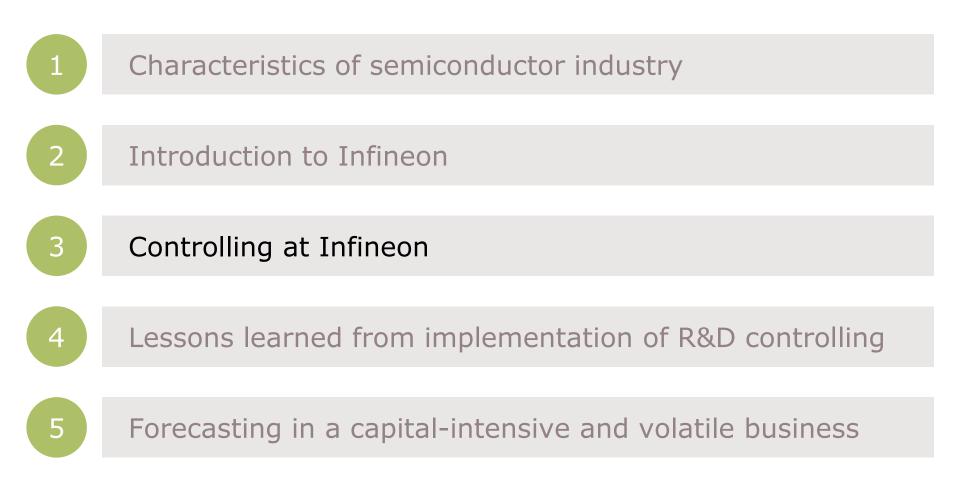
Source: IHS Markit, October 2016

Source: IHS Markit, July 2016

\* including SHHIC (in 2015, SHHIC was acquired by CEC Huada.)









### Main controlling KPIs at Infineon

Management Board	Segment Result Margin	Revenue growth		
	FCF from continuing operations	Return on capital employed		
Divisions				
Segment Result Margin	Revenue growth	Gross Margin	R&D-to-Sales	
Operations Central Functions				
CapEx-to-Sales	Gross Margin	G&A-to-Sales	S&M-to-Sales	
Employees				
Segment Result Margin*FCF from continuing operationsReturn on capital employed				

\* Segment result margin of respective Division or revenue-weighted average of all Divisions if employee works in Operations or Central Functions

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### Semiconductor industry characteristics

> Asset Intense Industry

- > Long Production Lead Time
- Dresden One Waferfab Operates is a 1,5 BillionUS\$ 24h/7d Asset to ppf SORT DIE BANK ASSEMBLY TEST Around 4-6 months cycle time [%///] 3% 2% 200 2010 2011 2012 10% -2% -20% -3% -4% 40% Norld -5% **Bullwhip effect** Semicon-ductor Equipment supplier OEM Customer Tier 1 Tier 2 Overshooting in the value chain
- > Significant Demand Fluctuation

> Early in the Value Chain

## Three major stakeholders in Infineon's planning process





Regional Account Teams

P&L Responsibility

Global Production and Distribution Network

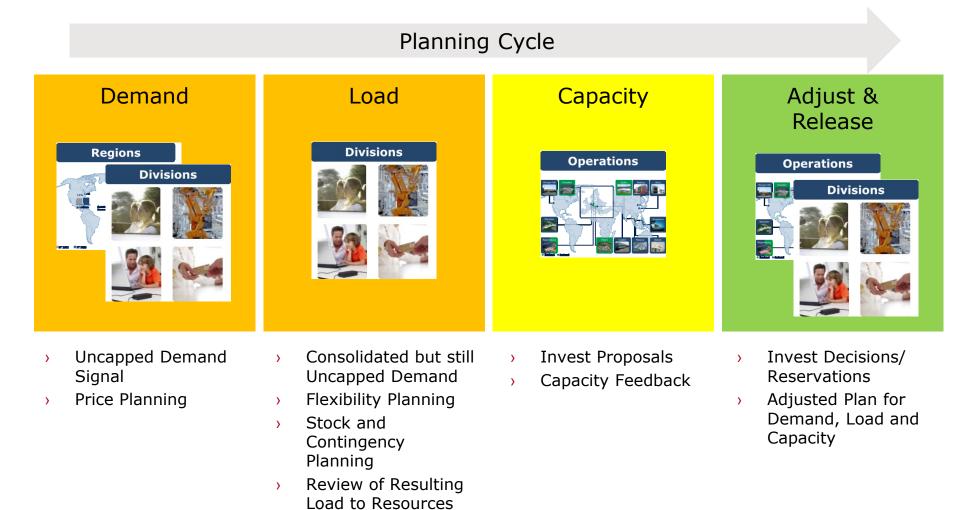
**Uncapped Demand** 

Consolidated Demand and Release

Capacity & Supply

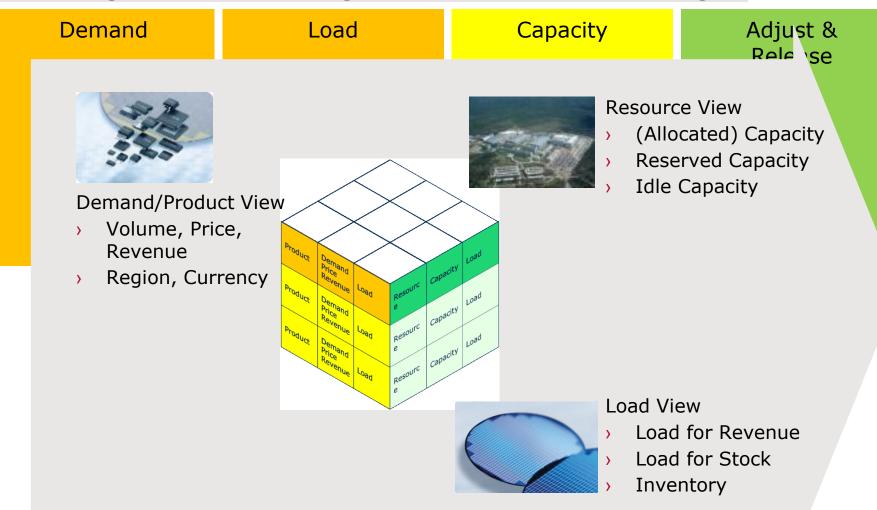
## Stakeholders are aligned within one planning cycle





## Multidimensional big picture within online planning cube for integrated decision making

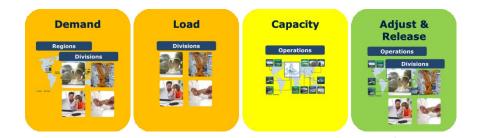




#### All Planning dimensions are available and aligned at all planning steps

### Adding Financial Key Parameters to Planning Cube translates it into a Financial Statement





Financial Parameters Sourced from Finance Systems

- > Price per Piece
- > Cost per Piece
- > Cost per Idle Capacity Unit
- > ...

Volume related inputs to Financial Forecast Statement

- > Revenue
- > Cost

. . .

Load

Load

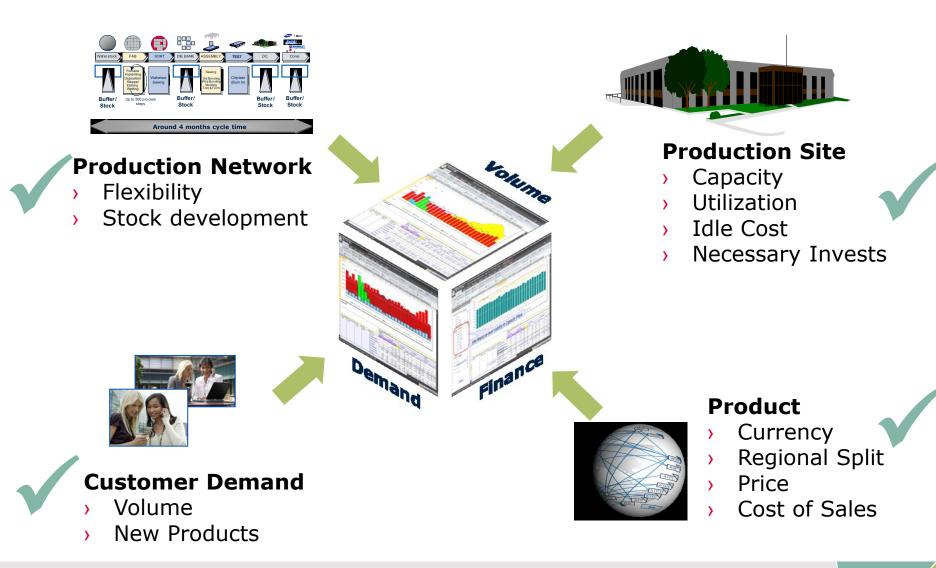
Capacity

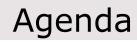
Capacity

- > Margin
- > Idle Cost
- Inventory
  Value

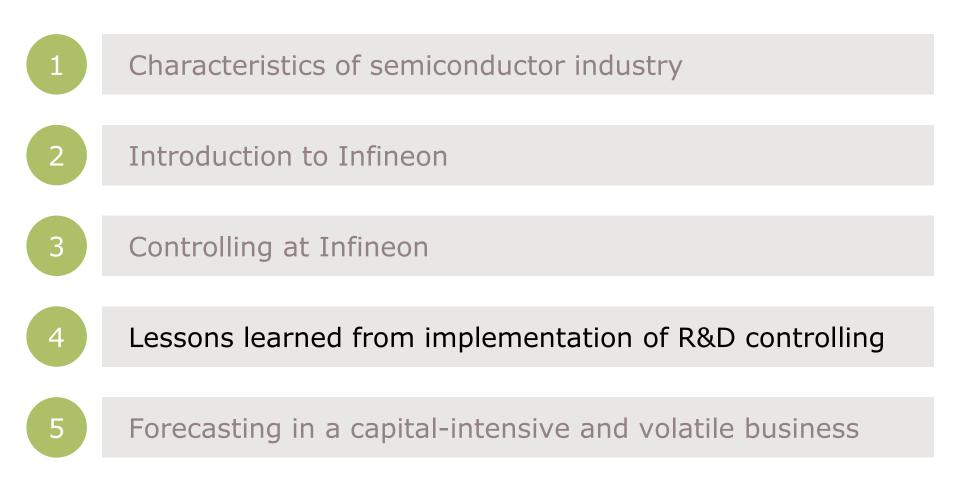
## Multidimensional picture for controlling landscape













### What projects do we invest our R&D money in and how profitable are they?

# The PPL lies at the core of our financial R&D project controlling



### Purpose of a Product Plan - PPL

PPL predicts the contribution to financial targets with respect to profitability and growth potential

PPL is a financial tool to support systematic reviews during the lifecycle of a product

PPL serves visibility for assigned entrepreneurs to react fast on increasing market dynamics A PPL is a joint commitment of...

- Sales & Marketing on future customer projects in terms of price, volume and product specifications
- Operations regarding future manufacturing cost
- R&D regarding project timeline, resource commitment and project cost development
- Finance

Replace excel by a system – the three key elements of ePPL 2.0











ePPL 2.0 DB



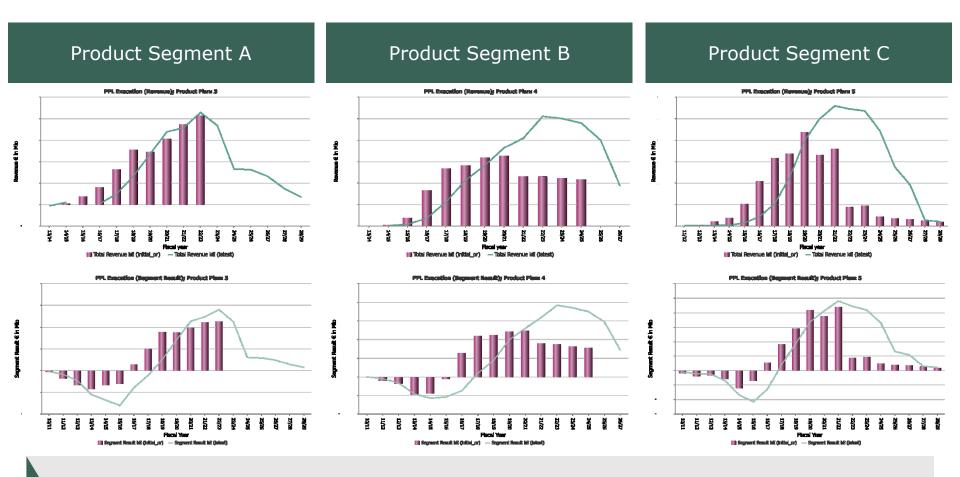
**BI** Portal







### **KPI: PPL Execution**

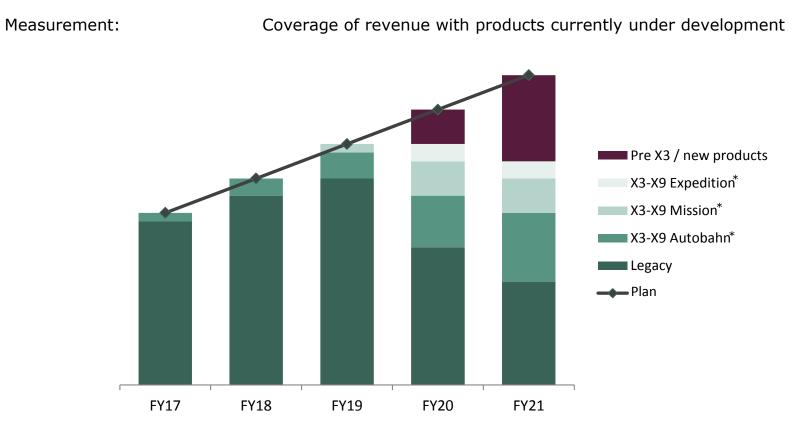


- Measure for project planning accuracy
- Measure for actual time-to-market

## MI: Strategic 5-year plan vs. R&D pipeline coverage



How much revenue is planned with products which are currently under development (Milestone 3- Milestone 9) during 5-year planning timeframe, what is the risk profile (execution, time to market) of the plan



Project categories: 1) Expedition: Dynamic boundary conditions, high level of uncertainties. 2) Mission: Possible changes to boundary conditions, significant but manageable uncertainties. 3) Autobahn: Stable boundary conditions, low level of uncertainties.

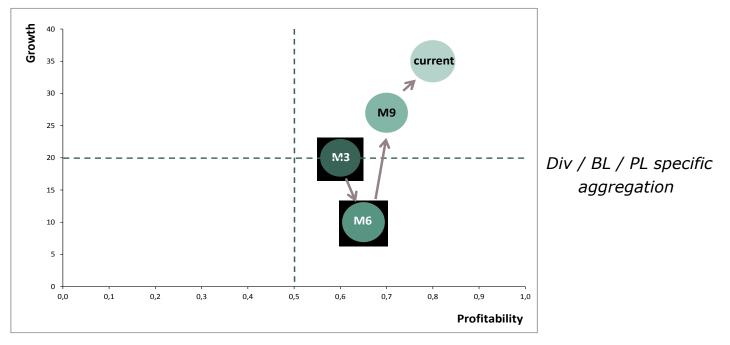


### KPI: Conversion rate of R&D into revenue

How does a project/product contribute to growth and profitability of a Division/Business Line/Product Line?

What is the conversion rate of an R&D investment into revenue, what is the profitability compared to the segment targets / expectation?

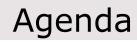
Measurement 1:Conversion rate of 1€ project specific R&D into X€ cum. revenueMeasurement 2:Øproduct-cost-margin of product/project compared to targets in 5y-planMeasurement 3:Planning stability between Milestone 3 and Milestone 9



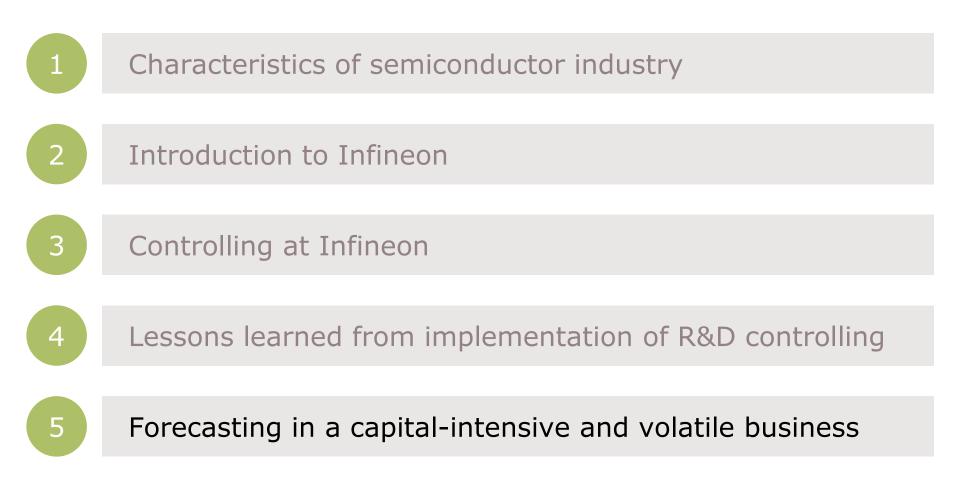


### Lessons learned during PPL roll out

- Ease of use and value add required to provide incentive for adoption "bottom up"
  - Certain simplifications to be jointly agreed in the finance community to reduce the degree of complexity
  - Data available in other systems needs to be automatically fed into the system to reduce workload
  - User interface needs to be convenient and fast
- Tone from the top both by the technical and finance side required to support roll out "top down"
  - Improved transparency in an area which is at the core of the decision power of the divisions not always welcome (potential fear of increased top down interference)
  - It takes a considerable time before contents provide full benefit in terms of controlling

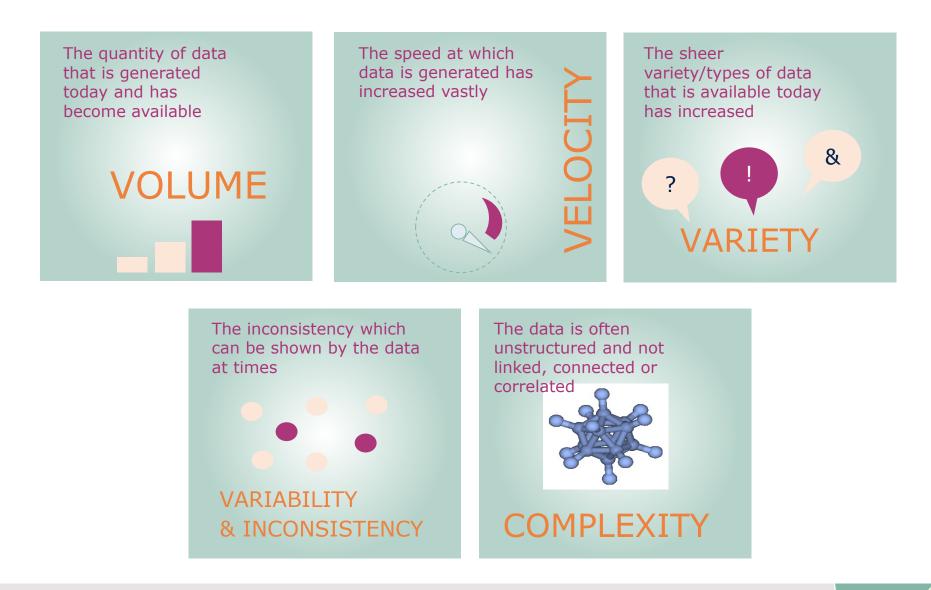






## Proliferation of data has the potential to change how we think of business strategy

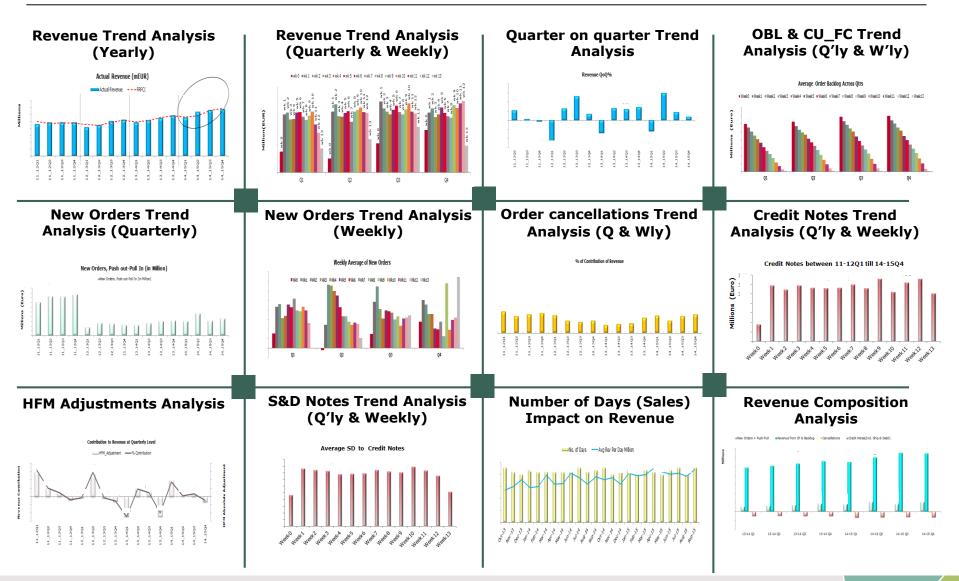




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#### Exploratory Data Analysis A few of the key data points considered

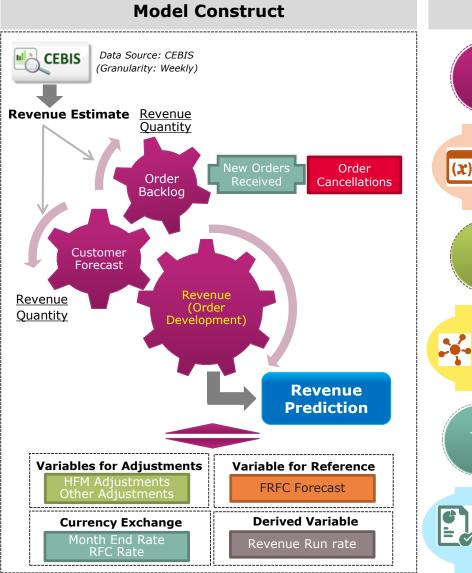




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### Infineon Group revenue prediction model construct



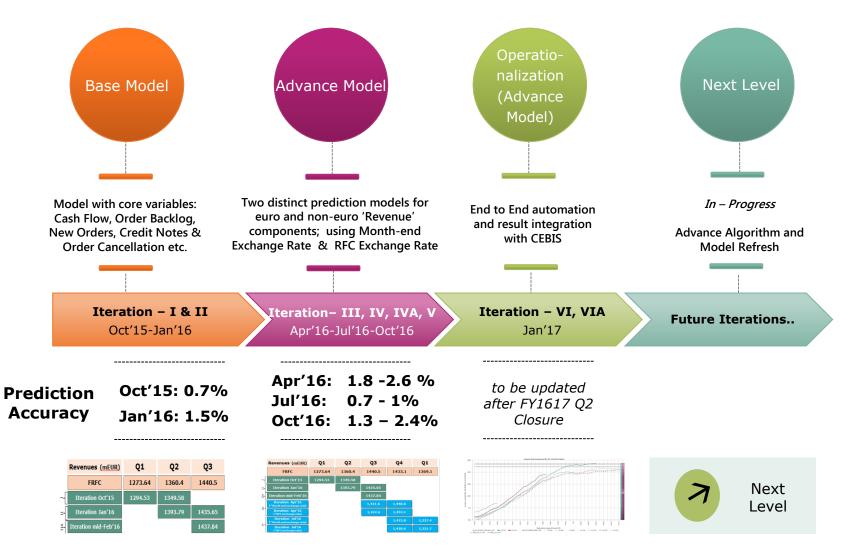
#### **IFX Group Prediction Model: Major Features**



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### IFX Group Revenue Prediction Model Multi-iterations with High Prediction Accuracy







### Thank you for your attention



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